Page 2, above the last full paragraph, insert:

--Summary of the Invention:--.

Page 5, above the first full paragraph, insert:

--Brief Description of the Drawings:--.

Page 5, above the fourth full paragraph, insert:

--Description of the Preferred Embodiment:--.

Replace the paragraph bridging pages 8 and 9 with the following:

--An access controller receives a data packet on a line (Medium Access Control MAC) from the network (LAN) and writes it via the internal bus 1 to the packet memory 44, which is likewise connected to the memory management unit 18. To match the data rate between the input and the internal bus 1, a bus access controller and a first-in-first-out memory 14 need to be provided. The respective access controller also handles the tasks associated with the transmission protocol, such as throughput control. Connected to the bus 1 are twelve access controllers for a speed of 100 megabit/s (FEMAC) and an access controller for one gigabit/s (GMAC). However, only either the twelve FEMACs or the GMAC are active in each case. Hence, all the units connected to the bus 1 perceive no difference as to